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-	17	DMA adj mode and CPU adj mode	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 14:03
-	2	(DMA adj mode and CPU adj mode) and status adj bit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 14:03
-	1	((DMA adj mode and CPU adj mode) and status adj bit) and @pd<20010228	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/07 18:48